

Group A
Assignment 3

Title : BCD Adder

Problem Statement : Design and realization of BCD adder using 4-bit Binary Adder (IC 7483)

Hardware and

Software requirements

Theory:

→ What is BCD Adder?

• The 4 bit binary adder IC 7483 can be used to perform addition of BCD numbers.

→ BCD addition rules:

i) Rule 1:

Sum ≤ 9 , carry = 0

eg: 3+6

	3	0	0	1	1
+	6	0	1	1	0
		1	0	0	1
		+ 0	0	0	0
		1	0	0	1

ii) Rule 2:

Sum > 9 , carry = 0

Eg: 6+8

$$\begin{array}{r}
 6 \quad 0 \quad 1 \quad 1 \quad 0 \\
 + 9 \quad 1 \quad 0 \quad 0 \quad 0 \\
 \hline
 \quad 1 \quad 1 \quad 1 \quad 1 \quad 0 \\
 + 0 \quad 1 \quad 1 \quad 0 \quad 0 \quad \rightarrow \text{adding } 6 \\
 \hline
 1 \quad 0 \quad 1 \quad 0 \quad 0
 \end{array}$$

iii) Rule 3:

Sum ≤ 9 , carry = 1

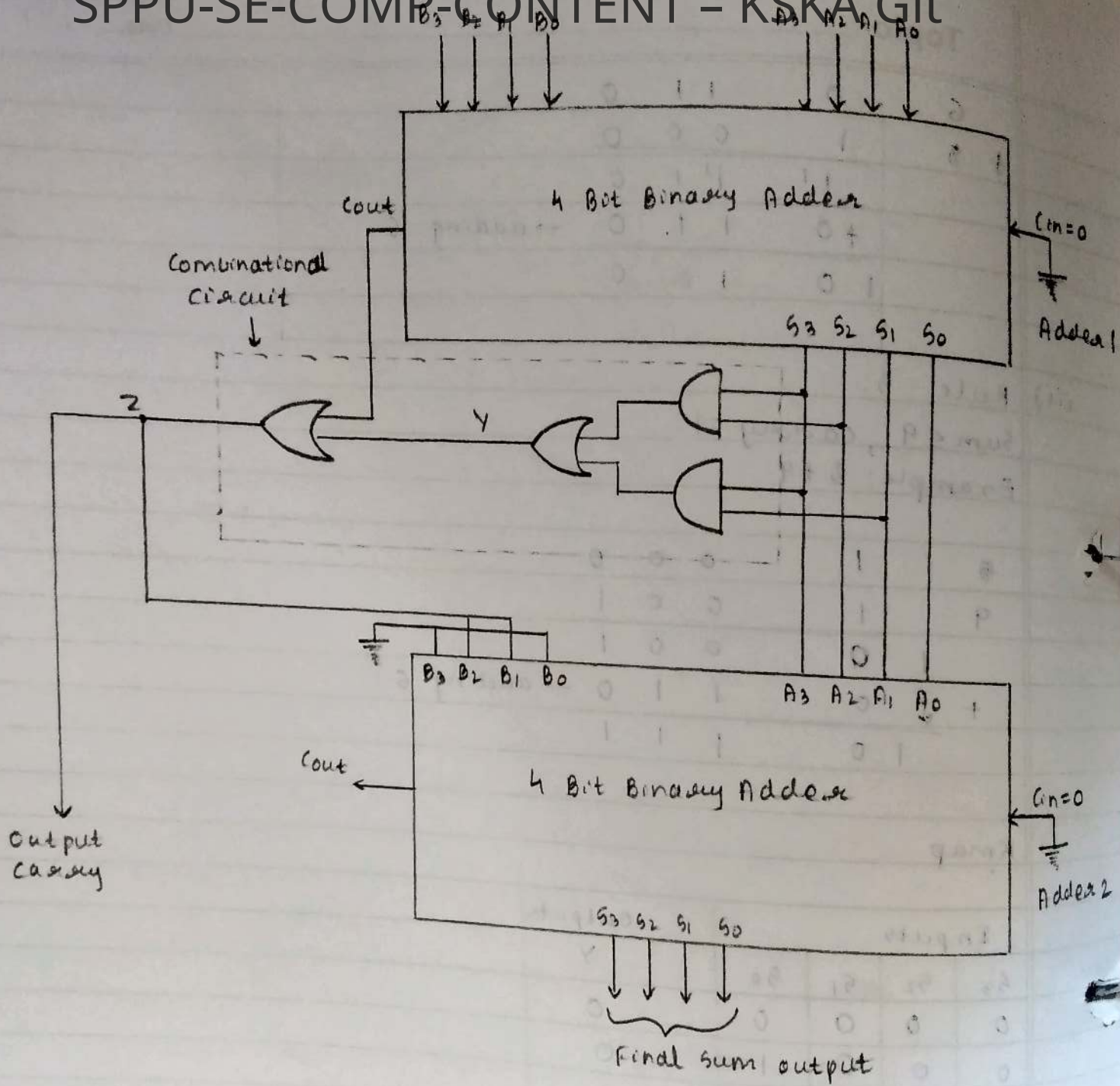
Example: 8 + 9

$$\begin{array}{r}
 8 \quad 1 \quad 0 \quad 0 \quad 0 \\
 9 \quad 1 \quad 0 \quad 0 \quad 1 \\
 \hline
 \quad 1 \quad 0 \quad 0 \quad 0 \quad 1 \\
 + \downarrow 0 \quad 1 \quad 1 \quad 0 \quad \rightarrow \text{adding } 6 \\
 \hline
 \quad 1 \quad 0 \quad 1 \quad 1 \quad 1
 \end{array}$$

→ Kmap

Inputs				Outputs
S ₃	S ₂	S ₁	S ₀	Y
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1

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1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

Y

	$S_1 S_0$				
$S_3 S_2$	$\bar{S}_1 \bar{S}_0$	$\bar{S}_1 S_0$	$S_1 \bar{S}_0$	$S_1 S_0$	
$\bar{S}_3 \bar{S}_2$	0	0	0	0	
$\bar{S}_3 S_2$	0	0	0	0	
$S_3 \bar{S}_2$	1	1	1	1	
$S_3 S_2$	0	0	1	1	

$$Y = S_3 S_2 + S_3 S_1$$

→ Applications

- It is used widely on the computers and calculators.
- Used on arithmetic logic unit.
- It accepts the binary coded form of decimal numbers.
- In applications where microcontrollers need to handle decimal arithmetic, BCD adders can be implemented to perform calculations accurately.
- It is used on digital clocks, electronic meters or any system that directly displays decimal values.

→ Conclusion:

We have designed and realized BCD adder using 4-bit Binary Adder (IC 7483)