

Modern Education Society's College of Engineering, Pune
Department of Computer Engineering
List of Assignments

Subject – Digital Electronics Lab (DEL)

Year – 2023-24

Class – S. E. A

Assignment No.	Title	P Batch	Q Batch	R Batch
1	Realize Full Adder and Subtractor using a) Basic Gates and b) Universal Gates	22/11/23	22/08/23	21/08/23
2	Design and implement Code converters-Binary to Gray and BCD to Excess-3.	29/08/23	29/08/23	28/08/23
3	Design and Realization of BCD Adder using 4-bit Binary Adder (IC 7483).	05/09/23	06/09/23	04/09/23
4	Realization of Boolean Expression for suitable combination logic using MUX 74151/DMUX 74154	08/09/23	12/09/23	08/08/23
5	Verify the truth table of one bit and two bit comparators using logic gates and comparator IC	12/09/23	12/09/23	11/08/23
6	Design & Implement Parity Generator using EX-OR.	26/09/23	26/09/23	11/08/23
7	Flip Flop Conversion: Design and Realization.	03/10/23	26/09/23	25/09/23
8	Design of Ripple Counter using suitable Flip Flops	03/10/23	03/10/23	16/10/23
9	Realization of 3 bit Up/Down Counter using MS JK Flip Flop / D Flip Flop	17/10/23	17/10/23	30/10/23
10	Realization of Mod -N counter using (7490 and 74193)	31/10/23	31/10/23	06/11/23
11	Design and implement Sequence generator using JK flip-flop.	06/11/23	21/11/23	21/11/23
12	Study of Shift Registers (SISO,SIPO, PISO, PIPO)	01/11/23	01/11/23	01/11/23

Subject Teacher

Prof. B.F. More