Digital Electronics and Logic Design

Unit I Minimization **Technique**

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CONSTRUCTION

CONSTRUCTION OF REAL PROPERTY.

Agenda

Logic Design Minimization Technique-Minimization of Boolean function using Kmap(up to 4 variables) and Quine Mc-Clusky Method

02

Representation of signed numbersign magnitude representation ,1's complement and 2's complement form

03 Sum of product and product of sum

Minimization of SOP and POS using k-

Signed Numbers

Signed numbers contain both sign and magnitude of the number. Generally, the sign is placed in front of number. So, we have to consider the positive sign for positive numbers and negative sign for negative numbers.

- There are three **types of representations** for signed binary numbers:
- 1. Sign-Magnitude form
- 2. 1's complement form
- 3. 2's complement form

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1. Signed Magnitude:

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Signed numbers contain sign flag, this representation distinguish positive and negative numbers. This technique contains both sign bit and magnitude of a number. For example, in representation of negative decimal numbers, we need to put negative symbol in front of given decimal number.

- Positive number is represented with '0' at its most significant bit (MSB).
- Negative number is represented with '1' at its most significant bit (MSB).

2. One's Complement of a Signed Binary Number

The 1's complement of a number is obtained by **complementing all the bits** of signed binary number. So, 1's complement of positive number gives a negative number. Similarly, 1's complement of negative number gives a positive number.

1. How to represent (-33)¹⁰ in 1's complement form?

33 is represented as (100001)² In 8 bit notation, it is represented as (0010 0001)² **Now, -33 is represented in one's compliment as (1101 1110)²**

2. How to represent (-1)¹⁰ in 1's complement form?

1 is represented as (001) ² **In 8 bit notation, it is represented as (0000 0001)**² **Now, -1 is represented in one's compliment as (1111 1110)2**

3. Two's Complement of a Signed Binary Number

To get 2's complement of a binary number, simply invert the given number and add 1 to the least significant bit (LSB) of given result.

Example:

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1. Find 2's complement of binary number 10101110.

Simply invert each bit of given binary number, which will be 01010001. Then add 1 to the LSB of this result, i.e., 01010001+1=01010010 which is answer.

Direct conversion

Signed binary number examples

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Examples:

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- **1. Represent (-12) in,**
- a. 8 bit signed magnitude form
- b. 1's complement form
- c. 2's complement form

2. Find out 2's complement of.

- a. 1110101110101
- b. 000101010010
- c. 101010101000

Boolean Algebra: It is mathematical system that defines a series of logical operations (AND, OR, NOT, etc) perform on set of variables (A,B,C, etc). Only two values(1 for high and 0 for low) are possible for the variable used in Boolean algebra.

Example:

F(A,B,C) = AB + A'C + B'C'

- **Variables**
- **Constant**
- **Complement**
- **Literals**
- **Boolean Function**

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Boolean Expression = Variables + Constants + Boolean operations

We use Boolean Expression to describe Boolean Function.

Properties of Boolean algebra

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Properties of Boolean algebra- DeMorgan's Theorem

$$
A \underbrace{\longrightarrow}_{B} \underbrace{\longrightarrow}_{A B} \underbrace{\overline{AB}}_{}
$$

is equivalent to

 $AB = A + B$

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is equivalent to

 $A+B = \overline{A} \ \overline{B}$

NAND = Bubbled OR NOR = Bubbled AND

Properties of Boolean algebra- DeMorgan's Theorem

Verification of De-Morgan's Theorem

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Boolean algebra Examples

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Examples:

- **Prove the following using De Morgans's Theorem**
- 1. $AB + CD = ((AB)' + (CD)')'$
- **2. (A+B) . (C+D) = ((A+B)' + (C+D)')'**
- **Minimize the expression**
- **1. Y = A'BCD' + BCD' + BC'D' + BC'D**
- **2. F = A + A'B + AB'**
- **3. X = A.(A'+B)**

Boolean Expression

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Based on the structure of Boolean expression, it is classified into two categories.

- 1. Sum Of Product (SOP)
- 2. Product Of Sum (POS)

Conversion of SOP form to standard SOP form or Canonical SOP form

Step 1:

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By multiplying each non-standard product term with the sum of its missing variable and its complement, which results in 2 product terms

Step 2:

By repeating the step 1, until all resulting product terms contain all variables

Example:

Convert the non standard SOP function $F = x y + x z + y z$ Sol:

$$
F = xy + x z + y z
$$

= xy (z + z') + x (y + y') z + (x + x') y z
= xy z + xy z' + xy z + xy' z + xy z + x' y z
= xy z + xy z' + xy' z + x' y z

The standard SOP form is $F = x y z + x y z' + x y' z + x' y z$

Conversion of POS form to standard POS form or Canonical POS form Step 1:

By adding each non-standard sum term to the product of its missing variable and its complement, which results in 2 sum terms

Step 2:

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Applying Boolean algebraic law, A + BC = (A + B) * (A + C)
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Step 3:

By repeating the step 1, until all resulting sum terms contain all variables

Example:

 $F = (A' + B + C) * (B' + C + D') * (A + B' + C' + D)$

In the first term, the variable D or D' is missing, so we add $D^*D' = 0$ to it. Then

 $(A' + B + C + D^*D') = (A' + B + C + D) * (A' + B + C + D')$

Similarly, in the second term, the variable A or A' is missing, so we add $A^*A' = 0$ to it. Then

 $(B' + C + D' + A^*A') = (A + B' + C + D') * (A' + B' + C + D')$

The third term is already in the standard form, as it has all the variables. Now the standard POS form equation of the function is

F = (A' + B + C + D) * (A' + B + C + D') * (A + B' + C + D') * (A' + B' + C + D') * (A + B' + C' + D)

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Convert of SOP form to standard SOP form or Canonical SOP form
Y= ABC + AB + BC' + A
Z= ABC + ABD + BCD' +AC'D'
X= A + B
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Convert of POS form to standard POS form or Canonical POS form
Y= (A+B) (B+C) (A+C)
Z= (A+B+C+D) (A'+C'+D) (B+C'+D')
```


M Notations: Minterm and Maxterm

SOP POS

A variable is in complemented form, if its value is In max term, each variable is complimented, if its assigned to 0, and the variable is un-complimented value is assigned to 1, and each variable is unform, if its value is assigned to 1.

complimented if its value is assigned to 0.

Standard Notation of SOP and POS

Example:

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The SOP function,
1. F(A,B,C) = A'B'C' + A'B'C + AB'C' + ABC' 
               0 1 4 6
            = mo + m1 + m4 + m6
   F(A,B,C) = \sum m (0, 1, 4, 6)2. F(A,B,C) = ∑ m (0, 2, 5, 6, 7)
           = mo + m2 + m5 + m6 +m7
           = A'B'C' + A'BC' + AB'C + ABC' + ABC
Exercise:
1. F = \sum m(1,3,4,6,7)2. F = \sum m (2, 4, 5)3. F = A'B'C + A'BC + AB'C + ABC'
```
The POS function, **1. F(A,B,C) = (A+B+C)*(A+B'+C)*(A+B'+C')*(A'+B'+C) 0 2 3 6 = M0 * M2 * M3 * M6 F(A,B,C) = Π M (0, 2, 3, 6) 2. F(A,B,C) = Π M (4,5,6,7) = M4 * M5 * M6 * M7 = (A'+B+C) (A'+B+C') (A'+B'+C) (A'+B'+C') Exercise: 1. F = Π M (1,3,5,7) 2. F = Π M (2,4,6) 3. F = (A+B+C) (A+B+C') (A+B'+C) (A+B'+C')**

Construct SOP and POS From a Truth Table

For SOP Expression,

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- 1. A circuit for a truth table with N input columns can use AND gates with N inputs, and each row in the truth table with a '1' in the output column requires one N-input AND gate.
- 2. Inputs to the AND gate are inverted if the input shows a '0' on the row, and not inverted if the input shows a '1' on the row.
- 3. All AND terms are connected to an M-input OR gate, where M is the number of '1' output rows.

The output of the OR gate is the function output.

Construct SOP and POS From a Truth Table

For POS Expression,

- 1. A circuit for a truth table with N input columns can use OR gates with N inputs, and each row in the truth table with a '0' in the output column requires one N-input OR gate.
- 2. Inputs to the OR gate are inverted if the input shows a '1' on the row, and not inverted if the input shows a '0' on the row.
- 3. All OR terms are connected to an M-input AND gate, where M is the number of '1' output rows.

The output of the AND gate is the function output

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Min and Max Term for given example

1. Write down SOP and POS expression from given truth tables

- 2. From following expression draw the truth table,
	- a. Y= A'B' + A'B + AB
	- b. F= A'B'C' + A'B'C + ABC

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c. Y = (A+B) (A'+B)
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d. Y= (A+B'+C+D') (A+B'+C+D) (A+B'+C'+D') (A+B+C+D') (A'+B'+C+D') (A'+B'+C'+D') (A+B+C+D)

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Minimization of SOP and POS using k-

Karnaugh Maps (K-map)

• A K-map is a collection of squares

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- Each square represents a minterm
- The collection of squares is a graphical representation of a Boolean function
- Adjacent squares differ in the value of one variable
- Alternative algebraic expressions for the same function are derived by recognizing patterns of squares (corresponding to cubes)

K-map for SOP

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K-map for POS

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Kmap Ploting from Truth Table

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Kmap Ploting from Truth Table

For 3 variables

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Kmap Ploting from Truth Table 20011

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- It is based on combining or grouping the terms in the adjacent cells of the kmap.
- **Way of grouping**

- We can group 1,2,4,8,16,32.....number of 1's or 0's.
- We can not group $3,5,7,...$ number of 1's or 0's.
- **1.Pair** : A group of two adjacent 1's or 0's.
- **2.Quad** : A group of four adjacent 1's or 0's.
- **3.Octet** : A group of eight adjacent 1's or 0's.

Rules for k-map grouping

- In grouping no 0s allowed for SOP and no 1s allowed for POS.
- Diagonal grouping not allowed.
- Only power of 2 numbers of cells in each group.
- Group should be as large as possible.
- Every 1 or 0 must be in at least one group.
- Overlapping of groups allowed.
- Wrap around is allowed.

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Number of groups should be as few as possible.

K-map Grouping

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1. **Pair** : A group of two adjacent 1's.

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K-map Grouping

2. **Quad** : A group of four adjacent 1's.

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K-map Grouping

3. **Octet** : A group of eight adjacent 1's.

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Y = C + D'

Example 1: Minimize the expression Y = A'B'C' + A'BC + AB'C' + AB'C using K-map method and draw the diagram.

Ans:

Y = A'B'C' + A'BC + AB'C' + AB'C

As it comprises 3 variables, we need a 3 variable K-map or 8 cells K-map. In the given expression the minterms are,

Example 2: Minimize the following expression using K-map method and draw the diagram. Y = ∑ m (1,5,7,9,11,13,15)

Ans: Y = ∑ m (1,5,7,9,11,13,15) Here we need a 4 variable K-map or 16 cells K-map.

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Y = C'D + BD + AD $= D (C' + B + A)$

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Minimization of SOP Expression

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Example 1: Minimize the following expression using K-map method and draw the diagram. Y = \sum **m** (3,4,6,7)

Example 2: Minimize the following expression using K-map method and draw the diagram. Y = ∑ m (0,1,3,4,5,6,7,8,11,15)

Don't Care Condition

Report Follows

- **Input Combinations for which the values of the Boolean expression are not specified are called as Don't care condition.**
- **It is indicated by 'X' or 'd' in the Truth Table.**
- **E.g. BCD to EX-3 Code Converter**

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Don't Care Condition

Example 1: Minimize the following logic function using K-map and realize using logic gates : $Y = \sum m (1,3,7,11,15) + d(0,2,5)$ [May 2018 - 4 Marks]

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ANTIFACTION

Example 1: Minimize the following expression using K-map method and draw the diagram. $Y = \sum m (0, 2, 5, 8, 11, 15) + d(1, 7, 14)$ [May 2016 4M]

Example 2: Minimize the following expression using K-map method and draw the diagram. $Y = \sum m (1,3,7,11,15) + d(0,2,5)$

Example 3: Minimize the following logic function using K-map and realize using logic gates : Y = ∑ m (1,5,7,13,15) + d (0,6,12,14) [May 2018 - 4 Marks]

Compute the minimal expression by multiplying the sum term of all the group

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(A'+B+C')

Y = (A+C) (A+B') (A'+B+C')

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Example 2: Minimize the expression using K-map, Y = ∏ M (1,5,7,13,15) + d(0,4,6,12,14)

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ANTIFACTURE

Example 1: Minimize the following expression using K-map method and draw the diagram. Y = ∏ M (1,2,3,7,10,11) + d(0,15)

Example 2: Minimize the following expression using K-map method and draw the diagram. Y = ∏ M (1,3,5,6,7,10,11) + d(2,4)

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Minimization of SOP and POS using k-

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• This method consists of two parts,

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- To find all the prime implicants
- To identify the essential prime impliants from the obtained prime implicants to form a Minimized expression.
- **Prime Implicants (PI)** A product term is called as a prime implicant if it cannot be combined with any other product term or group.
- **Essential Prime Implicants (EPI)** It is prime implicant in which one or more min term are unique i.e. it contains at least one min term which is not contained in any other prime implicant.

Example 1: Simplify the following function using Quine Mc-Cluskey minimization technique **Y= ∑ m (0,1,3,7,8,9,11,15) + d(2,4)**

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Y = CD + B'C'

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Example 1: Simplify the following function using Quine Mc-Cluskey minimization technique **Y= ∑ m (0,1,3,7,8,9,11,15) +d(2,4)**

Ans:

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Step 1: List all the min term in binary form

Step 2: Arrange the min terms in group of 1's in their binary representation.

Step 3: Compare every term of group n with each min term in the group n+1. If the binary representation of the terms differs by 1 bit in the same position, the two terms are combined. Put (\checkmark) in front of min terms combined and '-' in the different position.

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Step 4: Compare every term of group n with each min term in the group n+1.

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Report Follows

Step 5: List the all prime implicants (all unchecked terms from step 2 to step 4)

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Y= ∑ m (0,1,3,7,8,9,11,15) Step 6: Select EPI from all PI

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- 1. Put X in each row under the min terms contained in a particular PI.
- 2. Encircled min term 0 and 8 are contained in only one PI i.e. B'C' and the min term 7 and 15 are contained by only PI CD
- 3. Hence B'C' and CD are EPI.

Y (A,B,C,D) = B'C' + CD

Example 1: Simplify the following function using Quine Mc-cluskey method and draw the diagram. $Y = \sum m (0, 1, 2, 3, 5, 7, 8, 9, 11, 14)$ [Dec 2018 6M]

Example 2: Simplify the following function using Quine Mc-cluskey method and draw the diagram. Y = ∑ m (0,2,3,6,7,8,10,12,13)

Thank You…!!!