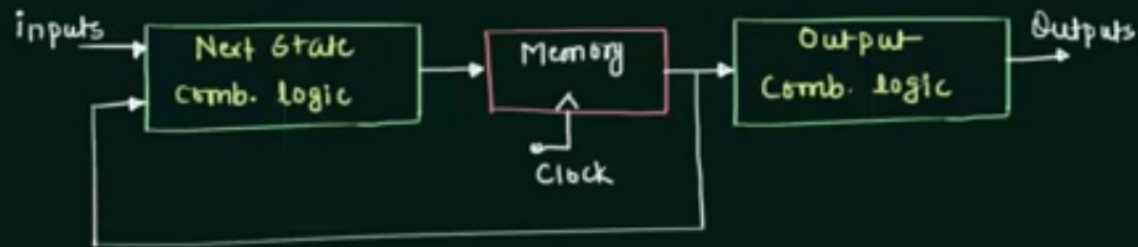


L-14 Mealy and Moore State Machines-1

Mealy and Moore State Machines (Part 1)

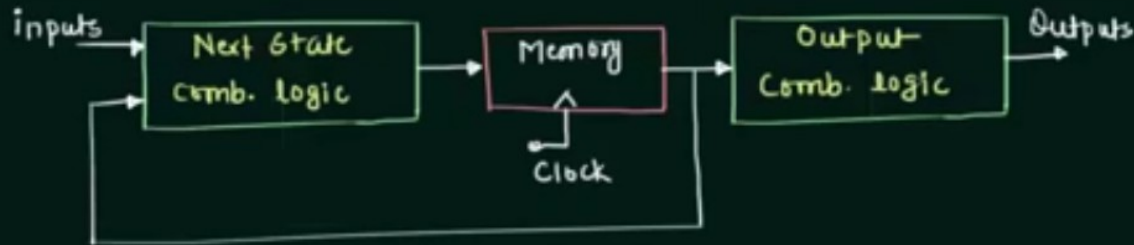
>> There are two models developed for representing synch. sequential circuits:



Mealy and Moore State Machines (Part 1)

>> There are two models developed for representing synch. sequential circuits:

- i) Moore Ckt / Moore State Mach.
- ii) Mealy Ckt / Mealy State Machine



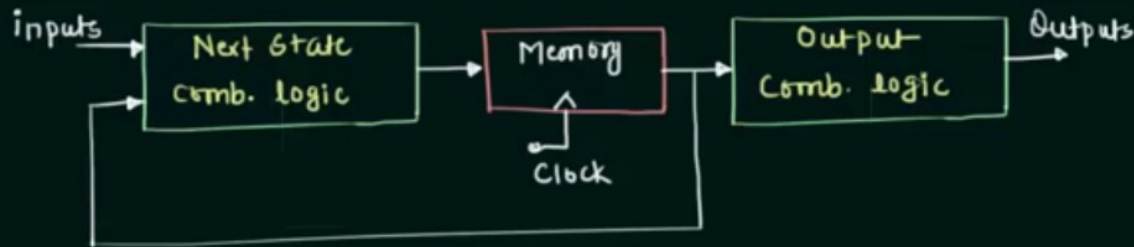
Mealy and Moore State Machines (Part 1)

>> There are two models developed for representing synch. sequential circuits:

i) Moore Ckt / Moore State Mach.

ii) Mealy Ckt / Mealy State Machine

* only in the way the o/p is generated



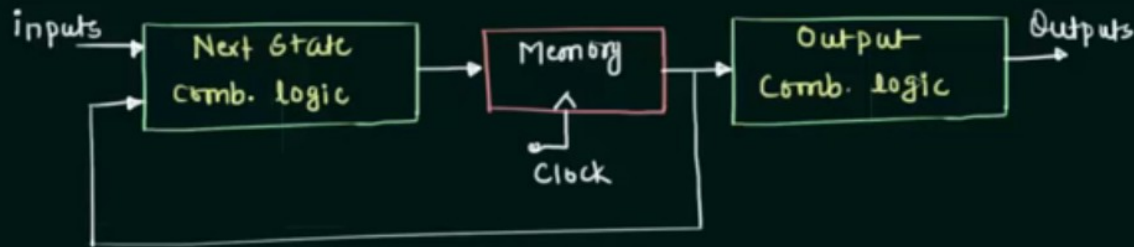
Mealy and Moore State Machines (Part 1)

>> There are two models developed for representing synch. sequential circuits:

i) Moore Ckt / Moore State Mach. :- o/p is the funⁿ of P.S. only

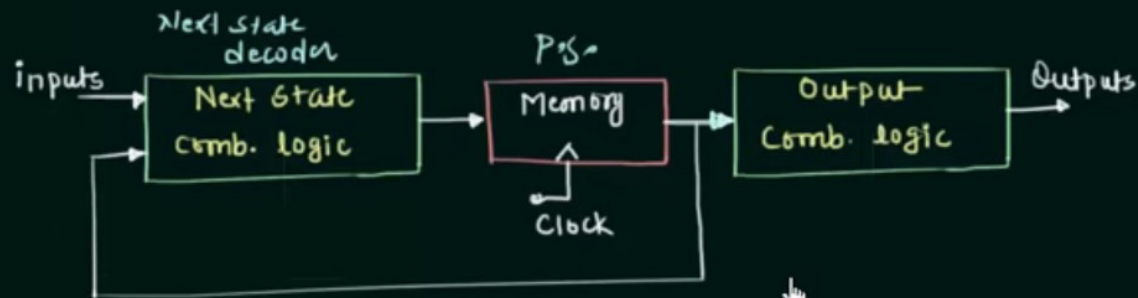
ii) Mealy Ckt / Mealy State Machine :- o/p is the funⁿ of P.S. as well as i/p

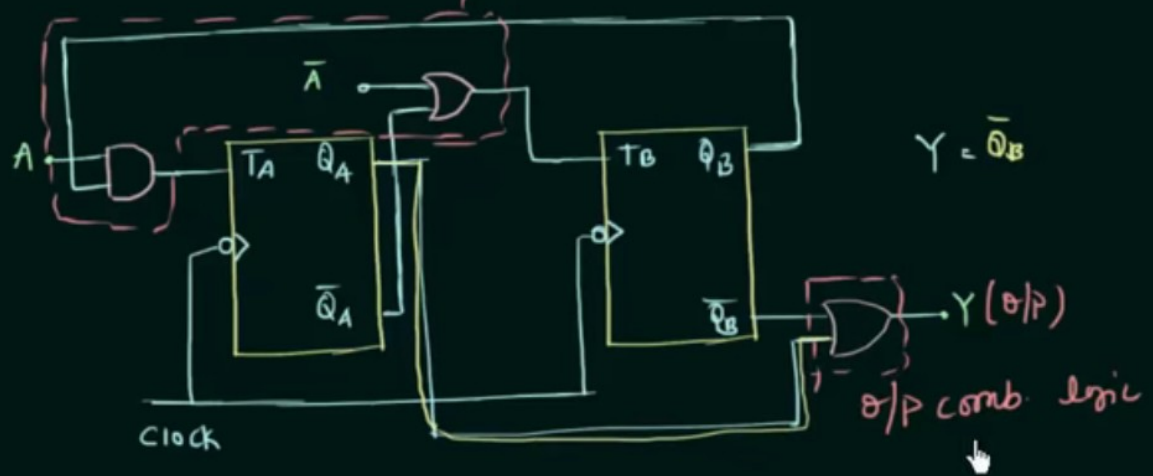
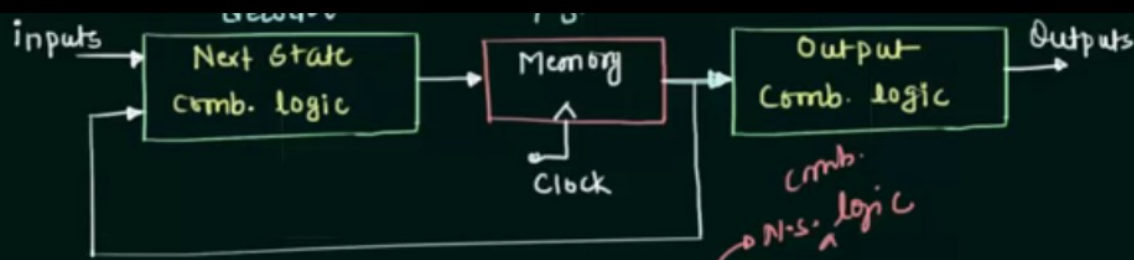
* only in the way the o/p is generated

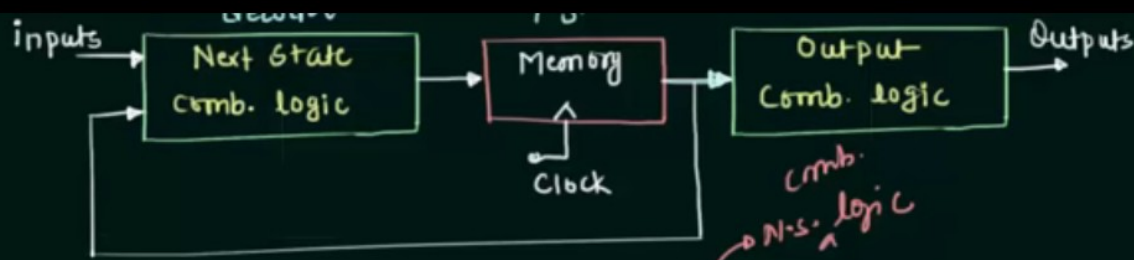


ii) Mealy Out/Clear state machine :- O/P is a function of P.S. as well as I/P

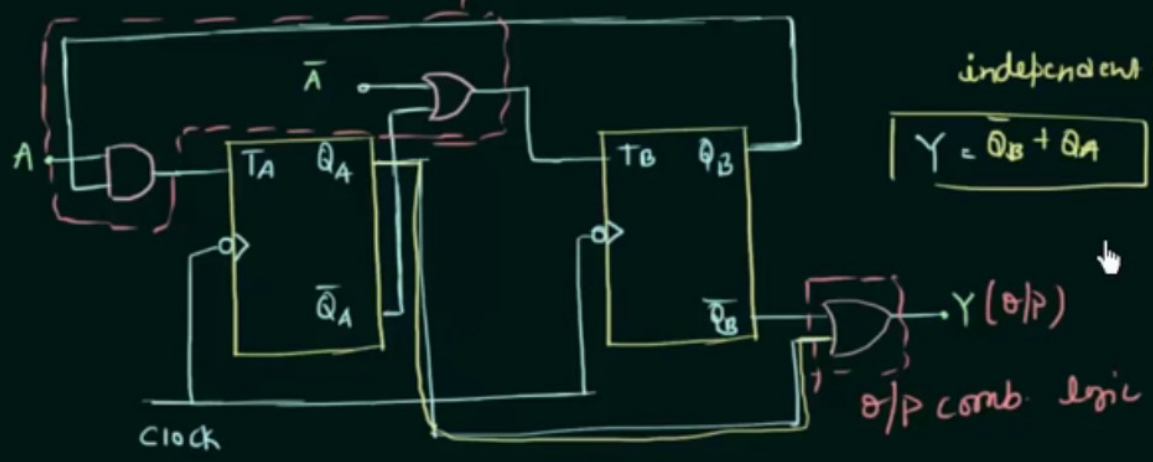
* only in the way the o/p is generated







comb. N.S. logic

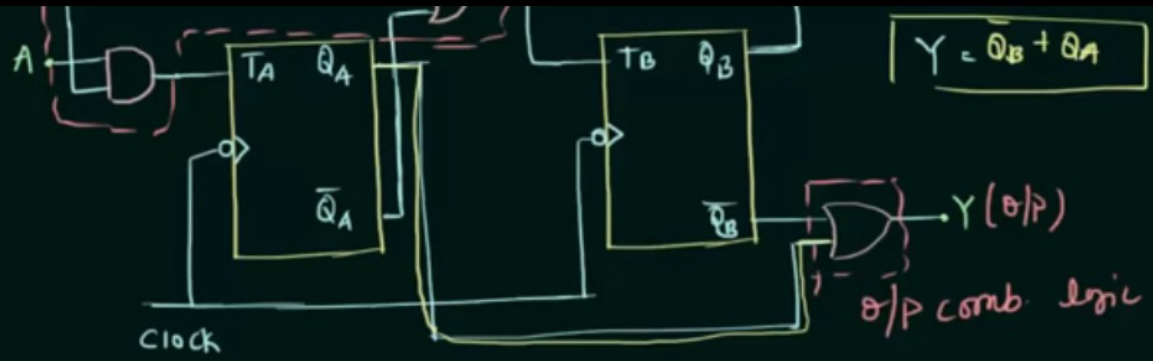


independent of A (i/p)

$$Y = \bar{Q}_B + Q_A$$

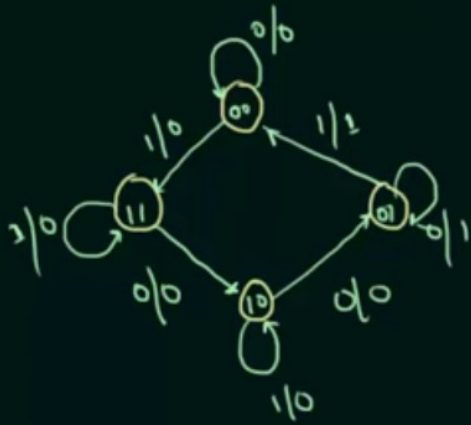
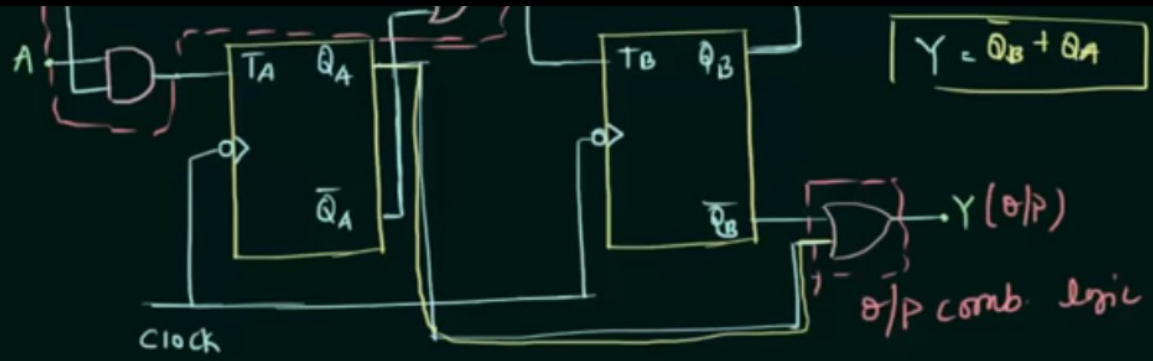
o/p comb. logic

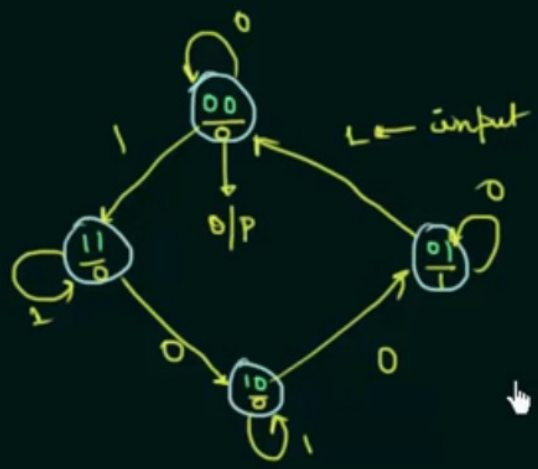
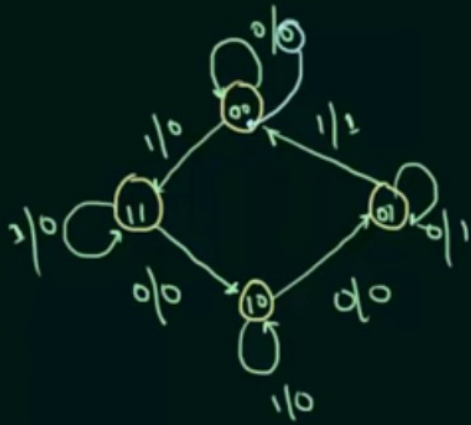
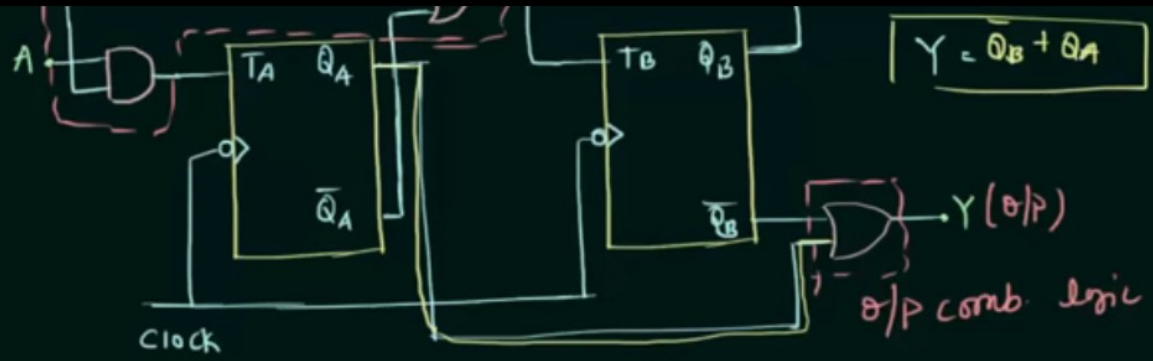


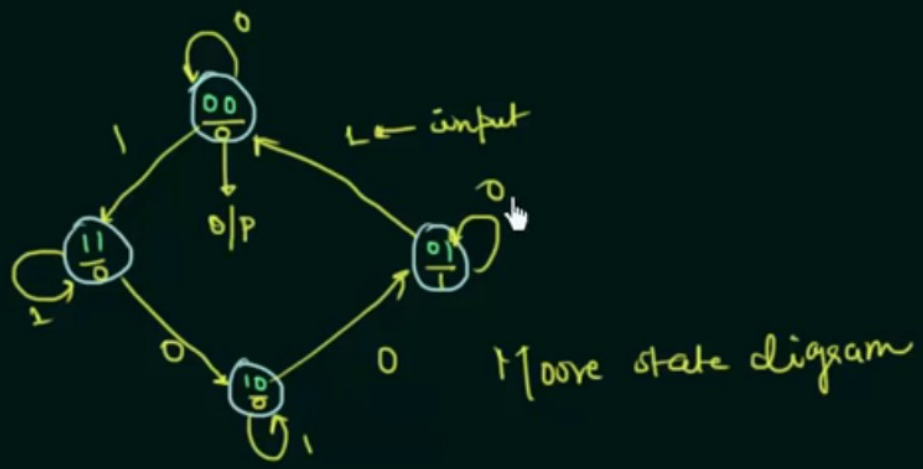
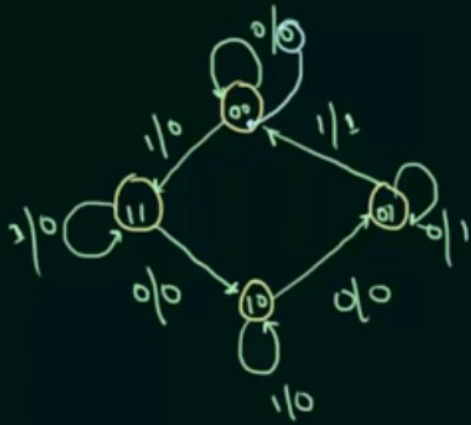
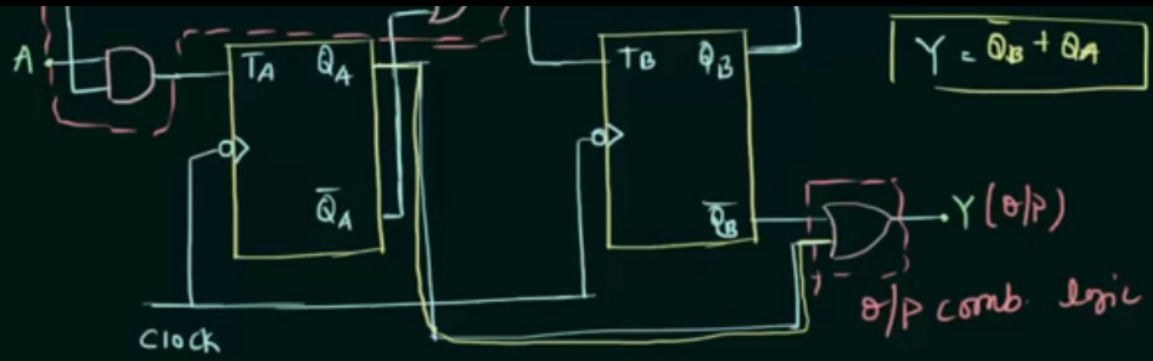


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• **Questions????**